	Type	L #	Hits	Search Text	DBs	Time Stamp	Error Definition
1	BRS	L1	835	DASD$ and flag$5	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM TDB	2002/02/12 09:21	
2	BRS	L2	422	1 and (access$4 same data same (set or sets)) and (stor$4 same devic$2)	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM TDB	2002/02/12 09:24	
3	BRS	L3	186	2 and fail$5 and ((mutipl$5 or different$) same stor$6)	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM TDB	2002/02/12 09:27	Truncation Overflow. Return string from Server is: 5`0`0`FAI
4	BRS	L4	245	2 and fail$5 and ((multipl$5 or different$) same stor$6)	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM TDB	2002/02/12 09:30	Truncation Overflow. Return string from Server is: 5`0`0`FAI
5	BRS	L5	211	2 and fail$5 and (multipl$5 same stor$6)	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM TDB	2002/02/12 09:32	Truncation Overflow. Return string from Server is: 5`0`0`FAI
6	BRS	L6	79	5 and (attempt$6 same fail$6)	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM TDB	2002/02/12 09:34	
7	BRS	L7	46	6 and (data adj set$5)	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM TDB	2002/02/12 09:35	Truncation Overflow. Return string from Server is: 5`2264462
8	BRS	L8	19	7 and (select$4 same stor$4 same devic$4)	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM TDB	2002/02/12 09:43	
9	IS&R	L9	3	("5765172").PN.	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM TDB	2002/02/12 09:44	
10	BRS	L10	1	5765172.uref,bi.	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM TDB	2002/02/12 09:44	

	Document ID	Issue Date	Title	Current OR △	Current XRef	Inventor
1	US 5325505 A	19940628	Intelligent storage manager for data storage apparatus having simulation capability	707/101	706/45 ; 706/934 ; 707/200	Hoffecker, John C. , et al.
2	US 4310883 A	19820112	Method and apparatus for assigning data sets to virtual volumes in a mass store	707/205		Clifton, Robert D. , et al.
3	US 4620295 A	19861028	Method for accessing a data set in a word processing system	707/531		Aiken, Jr., John A.
4	US 5369749 A	19941129	Method and apparatus for the direct transfer of information between application programs running on distinct processors without utilizing the services of one or both operating systems	709/104	709/310 ; 710/1	Baker, Ernest D. , et al.
5	US 5239649 A	19930824	Channel path load balancing, through selection of storage volumes to be processed, for long running applications	709/105	710/15 ; 710/18 ; 710/6	McBride, Gregory E. , et al.
6	US 5701502 A	19971223	Isolating a central processing unit from the operating system controlling said unit and its associated hardware for interaction of the unit with data handling apparatus alien to the operating system	709/201	709/1 ; 709/319 ; 710/110	Baker, Ernest Dysart , et al.
7	US 5283868 A	19940201	Providing additional system characteristics to a data processing system through operations of an application program, transparently to the operating system	709/227	709/246	Baker, Ernest D. , et al.
8	US 5155809 A	19921013	Uncoupling a central processing unit from its associated hardware for interaction with data handling apparatus alien to the operating system controlling said unit and hardware	709/227	709/201	Baker, Ernest D. , et al.
9	US 5388215 A	19950207	Uncoupling a central processing unit from its associated hardware for interaction with data handling apparatus alien to the operating system controlling said unit and hardware	709/229	703/22 ; 703/27 ; 709/221 ; 709/227	Baker, Ernest D. , et al.
10	US 5113522 A	19920512	Data processing system with system resource management for itself and for an associated alien processor	709/400	713/2 ; 714/10	Dinwiddie, Jr., John M. , et al.
11	US 5144692 A	19920901	System for controlling access by first system to portion of main memory dedicated exclusively to second system to facilitate input/output processing via first system	710/240	709/100 ; 710/1 ; 711/203	Baker, Ernest D. , et al.
12	US 5369767 A	19941129	Servicing interrupt requests in a data processing system without using the services of an operating system	710/264		Dinwiddie, Jr., John M. , et al.
13	US 4771375 A	19880913	Managing data storage devices connected to a digital computer	711/111	711/165	Beglin, Thomas W. , et al.
14	US 4583166 A	19860415	Roll mode for cached data storage	711/113	711/142	Hartung, Michael H. , et al.
15	US 4638424 A	19870120	Managing data storage devices connected to a digital computer	711/117	711/112	Beglin, Thomas W. , et al.
16	US 4574346 A	19860304	Method and apparatus for peripheral data handling hierarchies	711/117	711/112 ; 711/160	Hartung, Michael H.
17	US 4638425 A	19870120	Peripheral data storage having access controls with error recovery	711/133	711/162 ; 711/163 ; 711/173 ; 711/202 ; 714/10 ; 902/37	Hartung, Michael H.
18	US 5363497 A	19941108	System for removing section of memory from first system and allocating to second system in a manner indiscernable to both operating systems	711/153	709/216	Baker, Ernest D. , et al.
19	US 5325517 A	19940628	Fault tolerant data processing system	714/11		Baker, Ernest D. , et al.